

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. YOSHIDA et al.
 Serial No.: Not Yet Assigned
 Filed: On Even Date
 For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
 AND PROCESS FOR MANUFACTURING THE SAME
 Group: 2812
 Examiner: J. Hack



INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 AND 1.98

Assistant Commissioner for Patents
 Washington, D.C. 20231

October 13, 1999

Sir:

In the matter of the above-identified application, applicants are submitting herewith the attached form equivalent to Form PTO-1449 for the Examiner's consideration. It is noted that the present application is a divisional application of Serial No. 08/865,864, and the present information disclosure statement is being submitted to provide a listing of the references which were of record in the parent application. With regard to this, it is noted that copies are not being provided in accordance with 37 CFR 1.98(d) which states that:


"A copy of any patent publication or other information listed in an information disclosure statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the

statement and relied upon for an earlier
filing date under 35 USC 120."

Accordingly, it is respectfully requested that this
information disclosure statement be considered by the
Examiner, and that the references cited on the attached form
equivalent to Form PTO-1449 be listed by the Examiner in the
next Office Action. Applicants and the undersigned attorney
greatly appreciate the Examiner's cooperation and assistance
in this regard.

Respectfully submitted,

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Attachments